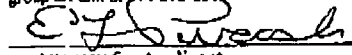


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Attorney for Applicant

Date: 17 March 2008

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Appl. No.:** 10/689,365  
**Applicant(s):** BEAUMONT, Mark  
**Filed:** 20 October 2003  
**Title:** Method For Load Balancing an N-Dimensional Array of Parallel Processing Elements  
**Art Unit:** 2195  
**Examiner:** KAWSAR, Abdullah Al  
**Docket No.:** PAT001062-000

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

OK TO ENTER

03/31/2008

/A.K./

**RESPONSE TO EXAMINER-INITIATED INTERVIEW**

In response to the examiner-initiated interview of 12 March 2008, please amend the application as follows:

**Amendments to the Claims** are reflected in the listing of the claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 10 of this paper.

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